Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13936	FPGA or (field adj programmable adj gate adj array)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 15:50
L2	1635	optimal adj configura$6	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 15:50
L3	8	1 same 2	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:03
L4	1	3 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:03
L5	22	1 and 2 and cache	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:08
L6	1455	(711/118).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:09
L7	1651	(711/154).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:10
L8	138	(711/166).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:10
L9	1392	(711/170).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/23 16:10